

ABSTRACT

A method and system for reducing power consumed by a magnetic memory is disclosed. In one aspect, the magnetic memory cells are coupled to a bit line and are associated with a plurality of digit lines. The method and system include providing a bit line  
5 current in the bit line and providing digit currents in parallel in the digit lines at substantially the same time as the bit line current. The digit and bit line currents allow the magnetic memory cells to be written to a plurality of states in parallel.